Technology Stack Working Group

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Introduction

The Technology Stack Working Group and its Taskforces incubate, design, test and recommend for adoption the components and combinations of technical elements that span all four layers of the ToIP stack and comprise a complete, interoperable instance of the model.

Scope

Define (directly or by reference) the technical standards, test suites, and interoperability certification standards for the Trust over IP architecture stack as defined in Hyperledger Aries RFC 0289 (or its successor as identified in the RFC document itself).

Meetings

Schedule:

US/EU: meetings are held bi-weekly, every second (TBC)

APAC: TBC

See the Meeting Page for agendas, notes and recordings for all meetings.

Deliverables

- TBC

Chairs / Leads

- Co-Chair: Dan Gisolfi (IBM)
- Co-Chair: Matt Davies (Finicity)
- Co-Chair: Darrell O'Donnell (Continuum Loop)
- Vice-Chair: Ajay Jadhav (AyanWorks)

How to join

You can join this WG by signing up for the Foundation mailing list at lists.trustoverip.org. Our mailing list is technology-stack-wg@lists.trustoverip.org.

Members as well as observers are welcome (with the important caveat below).

Participation

For the protection of all Members, participation in working groups, meetings and events is limited to members, including their employees, of the Trust over IP Foundation who have signed the membership documents and thus agreed to the intellectual property rules governing participation. If you or your employer are not a member, we ask that you not participate in meetings by verbal contribution or otherwise take any action beyond observing.